NOHAU

Cut Your Development Time !

Nohau EMUL-XA-SCC Pinouts
This emulator supports the Philips HCC, H3, and H4 microcontrollers. This is the layout and pin assignment for this emulator and is the top view of the target.

Version 4.0
May 20, 2000

1.900"
(48.26mm)

.100"
(2.54mm)

2.000"
(50.80mm)

.100"
(2.54mm)

This diagram is the top view of an SCC, H3, or H4 target. An example of this is the FDI XTEND-SCC evaluation board.

The Nohau emulator will plug into this footprint.

The 4 connectors on the emulator are female. The 30 pin is Samtec SSW-115-22-G-D. The 20 pin is SSW-110-22-G-D.

Top connector (pins 99–82 / 100–81):

99: P0.6_SCPTx — 82
Top row labels: P0.6_SCPTx, RxD0_L1RxD, P0.5_RTClk0_L1Clk, P0.3_CD0_L1SY1, P0.1_RTS0_L1RQ, VDD, P2.7_Sync3_BRG3, P2.5_CTS3, P2.3_ComClk_TRClk3, P2.1_TxD3

100 — 81
Bottom row labels: P0.7_SCPRx, SCPClk, TxD0_L1TxD, P0.4_TRClk0_SDS1, P0.2_CTS0_L1GR, P0.0_Sync0_BRG0_SDS2, VSS, P2.6_RTS3, P2.4_CD3, P2.2_RTClk3

Left connector (pins 1–2 ... 29–30):

1	2
VSS	VDD
A0	A1
A2	A3
A4	A5
A6	A7(A21_A22)
A8(A19_A20)	A9(A0_A18)
A10(A1)	A11(A2)
A12(A3)	A13(A4)
A14(A5)	A15(A6_A22)
VSS	VDD
A16(A7_A20_A21)	A17(A8_A18_A19)
A18	A19
D0	D1
D2	VSS
VDD	D3

29   30

Right connector (pins 80–79 ... 52–51):

80	79
P2.0_RxD3	Int0
CD1_Int2	VDD
VSS	P1.7_BRG2_Sync2
P1.6_RTS2	P1.5_CTS2
P1.4_CD2	P1.3_TRClk2
P1.2_RTClk2	P1.1_TxD2
P1.0_RxD2	P3.7_Int1_TRCLk1
P3.6_TxD1	P3.5_RxD1
P3.4_CTS1	P3.3_Timer1_BRG1_Sync1
VDD	XTALOUT
XTALIN	VSS
P3.2_Timer0_Resetout	P3.1_CS5_RAS5_RTS1
P3.0_CS4_RAS4_RTClk1	Reset_In
BLE_CASL	BHE_CASH
WAIT_SIZE16	OE

52   51

Bottom connector (pins 32–50 / 31–49):

32 — 50
Top row: D5, D7, D9, D11, D13, D15, VSS, CS3_RAS3, CS1_RAS1, WE

31 — 49
Bottom row: D4, D6, D8, D10, D12, D14, VDD, ClkOut, CS2_RAS2, CS0