

Application Note **MCS-51 ATMEL with ERAM** **September 10, 2002**

There is a problem with on-chip XDATA “ERAM” emulation and this can effect the access of off chip XDATA memory also. If you are experiencing a problem with this memory either not showing up or being of wrong size you need to do the following:

Add a register to the register display by right-clicking in the register window and select “Add Register” now set this up with the following information, name = CFG_CHIP, address = 0xFF, space = SFR , size = 8, click the enable the reset value box and set the value to 0x0C, then click OK. Once you have done this then click on the reset button on the tool bar and the system should start working correctly.

NOTE:

It is advised that you use the Config | Save Settings at this time so that this configuration will be saved for next time you start SeeHau.